

Practitioner's Docket No.:

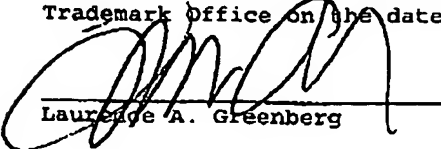
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 Laurence A. Greenberg

 January 31, 2006
 Date
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/623,843 Confirmation No.: 6526
 Applicant : Veronika Polei, et al.
 Filed : July 21, 2003
 Title : METHOD FOR FABRICATING A BURIED BIT LINE
 FOR A SEMICONDUCTOR MEMORY
 Art Unit : 2823
 Examiner : Khien D. Nguyen
 Customer No. : 24131

AMENDMENT under 37 CFR § 1.116

Mail Stop Box AF
 Hon. Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

S i r :

Responsive to the Advisory action dated January 19, 2006, and further responsive to the Office action dated October 31, 2005, kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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 OKAY TO ENTER
 K.N.
 02/09/06